

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10053970	FILING DATE 01/22/2002	CLASS 438	SUBCLASS 48	GAU 2812	EXAMINER LONG T.R.
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**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

GERMANY 101 02 432.0 01/19/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO MUH-12050
TITLE : Test circuit for an analog measurement of bit line signals of ferroelectric memory cells U.S. DEPT. OF COMM /PAT & TM-PTO-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Fig.s.Drwg.	Print Fig.
		Primary Examiner		
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		
Application Examiner				
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